

Claims

1. A power amplifier comprising:

a plurality of amplification stages arranged on a die to permit an RF signal flow from one stage to the next stage with said signal flow being characteristic of a zig-zag pattern; and

a DC bias circuitry centralized on said die, said circuitry providing a bias feed to each of said amplification stages;

wherein the plurality of amplification stages comprise a plurality of transistors each having an input and an output, the transistors arranged in a folded configuration;

wherein the plurality of amplification stages further comprise a compensation network coupled to a shared input of a transistor pair, signal splitter circuitry for providing substantially in-phase signals to the transistors; and combiner circuitry for in-phase combining outputs of the transistors.